Shivani Mahesh Deosthale

Address: 602, C1, Vedant Complex, Vartak Nagar, Thane (W), 400606

Maharashtra, India. 400606

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Professional Aspiration:

As a dedicated teacher and mentor.

Teaching Approvals:

● Full time Assistant Professor in Information Technology as per letter No.: CONCOL/IS/ECD/2012-13/18225 dated 26th April 2013 by University of Mumbai

● Full time Lecturer in Electronics Engineering as per letter No.: CONCOL/SA/7776 dated 13/11/2007 by University of Mumbai

Professional Experience:

1. Working as an Assistant Professor in K. J. Somaiya College of Engineering from 6th March 2024 in Computer Engineering Department till date.
2. Worked as Visiting Faculty in K. J. Somaiya College of Engineering Since 4th September 2023 to 5th March 2024 (6 months) in Computer Engineering Department.
3. Worked as Visiting Faculty in K. J. Somaiya College of Engineering since 16th January 2023 to May 2023 (3.5 months) in department of Information Technology.

4. Independent Initiatives (2017 till 2023)

✓English to Marathi Translator for NPTEL courses (2020-22)

✓ Published Diwali Magazine "SAACHI" for children in 2019

✓Association with JugaadFunda LLP, Pune, as a Certified Mentor. Completed "Train the Trainer" programme and "Design Thinking" Course.

5. Service:(1999-2017)

✓ Shah and Anchor Kutchhi Engineering College (SAKEC), Mumbai
Assistant Professor since 22 January 2001 to 19 January 2017 (16 years)

Major responsibilities –
- Teaching & Mentoring
- NBA and ISO coordinator
- UG and PG Project Coordinator
- UG Examination paper setting for Mumbai University

✓ Datta Meghe Engineering College, Navi Mumbai
Lecturer since August 2000 to January 2001 (5 months)

Major responsibilities – Teaching & Mentoring

✓ Indian Institute of Technology, Mumbai
Research Assistant since January 1999 to August 2000 (20 months) in Electrical Engineering department.

Major responsibilities – Technical assistance for the project on Optically Controlled Printed Semiconductor Antenna with the Department of Electronics (DoE) Delhi at IIT Bombay.

Additional activities

Papers Published:

1. Harsh Varudkar, Shivani Deosthale, Jalpa Mehta, "Collaborative Recommendation System Based On Hadoop", January 2017, volume 6 Issue 1, GJRA - GLOBAL JOURNAL FOR RESEARCH ANALYSIS, ISSN: 2277-8160, PP: 102-103

2. Smitesh Salian, Shivani Deosthale, Kranti Ghag, "A Novel Approach to

Resist Shoulder Surfing Attack", Volume 3 Issue 5, Sep - Oct 2016, International Journal of Computer Techniques (IJCT Journal), ISSN :2394-2231, PP: 37-40

3. Prathamesh Churi, Vaishali Ghate, Kranti Ghag, "Jumbling-Salting: An improvised approach for password encryption", November 2015, International Conference on Science and Technology (TICST)

4. Komal Waykole, Shivani Deosthale, Smita Bansod," Secure Dynamic Groups Auditing Service with Group signature for Cloud Storage", June 2015, Volume 3 Issue 6, International Journal on Recent and Innovation Trends in Computing and Communications (IJRITCC), ISSN:2321-8169, PP:4100-4103.

5. Pallavi Marathe, Shivani Deosthale, Rashmi Chavan," Two level Security for Cloud Storage with Data Deduplication'', June 2015, Volume 3 Issue 6, International Journal On Recent and Innovation Trends in Computing and Communications (IJRITCC), ISSN:2321-8169, PP:4090-4093.

6. Rohini Gaikwad, Vaishali Ghate, Jalpa Mehta," Independent Access to Encrypted Cloud Databases", Journal of Research in Computer Application and Management, ISSN 2231-1009, Volume 5, Issue 6, June 2015.

Online Certification:

1. Completed NPTEL online course conducted by IIT Roorkee on "Introduction to Cryptography" from July to September 2016 with score of 76%
2. Completed Coursera online course sponsored by Somaiya Vidyavihar University, on "AI for everyone" offered by Deeplearning.AI on May 15, 2023 with 92.50%

Seminars Organized
- Three-day workshop on VLSI design
- one-day Workshop on Software Testing Automation Tools
- One-week ISTE STTP on Wireless Communication & Mobile Technology

Seminars Delivered

- A seminar on Rules and layout in VLSI at SAKEC, Mumbai.
- A seminar in Bluetooth technology in Mumbai.
- A seminar on VHDL and practices in Fr. CRCE, Bandra, Mumbai.
- Workshop on LaTeX in ISTE approved STTP in SAKEC, Mumbai.

Seminars Attended (2001-2017)

● One-day workshop on VLSI design with VHDL in TSEC, Bandra.

● CEP four weekends on Elements of microelectronics in IIT Mumbai.

● One-day workshop on Advanced digital communication in VESIT, Chembur

● ISTE- STTP for two weeks on Digital communication with recent developments in DJSCE, Vile Parle.

● One-day workshop on Power electronics in Fr. CRCE, Bandra.

● AICTE-ISTE STTP one-week seminar on VLSI & embedded system in Fr.

  CRCE, Bandra.

● ISTE STTP one week on Effective communication and life skills in DJSCE, Vile Parle.

● One-day orkshop on Microprocessor and microcontroller in SAKEC, Chembur.

● Four-day workshop on Best practices in Network simulator-2 in St. Xavier's Institute, Mahim.

● One-day workshop on Advanced Networking technology in SAKEC, Chembur.

● ISTE STTP one week on Presentation and Effective teaching techniques in SAKEC, Chembur.

● One-day Faculty development programme: Workshop on Emotional Intelligence in SAKEC, Chembur.

● One-day Workshop on Software Testing Automation Tools in SAKEC, Chembur.

● ISTE Workshop on Software Development Techniques for Teachers of Engg & Science Institutes: IIT-B & K. J. Somaiya College of Engineering, Vidyavihar

● ISTE STTP on Wireless Communication & Mobile Technology in SAKEC.

● ISTE STTP on research Methodologies in K.J.Somaiya I.T., Sion

● ISTE STTP on Advanced Communication Systems in SAKEC

● ISTE STTP on Research Methodologies and recent trends in SAKEC

● ISTE STTP on Cyber Security Tools and Techniques in SAKEC

● National 3-day workshop on Cyber security and Hacking tools in RAIT, Nerul

Subjects Taught (UG&PG)

✓ Information Theory and coding

✓ Programming in Python

✓ Information & Network Security

✓ Digital Design

✓ Analog & Digital Communication

✓ Data Communication & Computer Networks

✓ Wireless Communication

✓ Mobile Computing

✓ Fiber Optics Communication

✓ Electronic Devices & Circuits

✓ Network design and Management

✓ Enterprise Security and Risk Management

✓ C programming

Educational Qualification:

Degree	Institute	Board/ University	Year	%
M.E.(Electronics)	Fr. CRCE, Bandra	Mumbai University	2008	71.62
B.E. (Electronics)	DMCOE, Navi Mumbai	Mumbai University	1998	62
HSC	KET's Vaze college, Mulund	Maharashtra Board	1994	80.50

Hardware/Software literacy:

- Programmable devices and Hardware Descriptional Language: CPLD, Xilinx, Spartan – VHDL
- Open-source electronics platform: Arduino
- OS: Windows, Linux
- MATLAB, C, Python
- MS Word, Excel, PowerPoint
- Technical Writer/Reference Manager: Overleaf, Mendeley, Grammarly, Turnitin

Personal Details:

- Date of Birth: 7th January 1977
- Marital Status: Married
- Languages Known: English, Marathi, Hindi, Sanskrit
- Blood Group: O - RH negative
- Major Strengths: Integrity, Dedication
- Extra-Curricular Activities: Articles and poem writing, Yoga