Somaiya Vidyavihar University

Name:	Dr. Seema Prashant Talmale		E-mail:seematalmale@somaiya.edu
Contact No:	**+91 22 66449223**		
Department/Section: Electronics Engineering			
College: K J Somaiya College of Engineering			
DOJ Somaiya:05/08/2003	Career Experience:20 Yrs	Industry Experience: _Yrs	Teaching Experience: 20Yrs
Present Academic Designation: Assistant Professor		Present Administrative Designation:Asso.HOD (Principal/Vice-Principal/ Associate Dean/ HOD etc)	

Area of research/specialization and Courses Delivered
Research domain/interests/areas

Research domain/interests/areas
1. Error correcting codes
2. Microwave and fibre-optics
3. Power Electronics
4. Engineering Electromagnetics
5. Digital Signal Processing
6. Basics of Communication Engineering
7. Industrial Electronics
8. EEEE
9. Electric Vehicle

Courses Delivered:

Power Electronics, B.E.E., Engineering Electromagnetics, Industrial Electronics, ECAD-II, Microwave, Filter Design, Digital Signal Processing, Basics of Communication Engineering, Industrial Electronics, Electric Vehicle, Power Electronics and Drives, EEEE, Discrete Mathematics

Recognition as a teacher by any University	UG: Yes	PG: Yes	Ph.D : Yes/No
Details of Recognitions			

Details of Recognitions
1. University of Mumbai
2. Somaiya Vidyavihar University

Education					
Examination	Name of the Degree	University/Board	Institute/College	Year	CPI/SPI/ %Marks
Ph.D	PhD	Mumbai	Fr.CRCR, Bandra(W), Mumbai	2022	
PG	M.Tech	Mumbai	VJTI, Mumbai	2010	9.1
UG	BE	Nagpur	G.H. Raisoni College of Engineering, Nagpur	2001	70.80%
Diploma	-				
NET/SET/Other	-				

Notable Experience Details					
Sr. No	Name of the organization	Designation	Date of Joining	Date of Leaving	Experience (Years)
1.	K J Somaiya College of Engineering	Lecturer, Assistant Professor	05/08/2003	Till date	20
2.	-				

Somaiya Vidyavihar University

Research Accomplishments and Projects		
No of students pursuing Ph.D as on date:-		No of students completed Ph.D as on date:-
No of students completed PG thesis / Project work as on date:-		No of students / groups completed UG projects as on date:-
Publications Total:9	Number of Peer review Journal papers:4	Number of Conference papers:5

Details of Publications:

International Journals

1. "Distance Increasing Mapping for Variable Distance Block Code", IET Communications, UK, June 2020).

2. "Distance Preserving Mapping and Simple Intelligent Decoder for Modified Linear Block Code", Information Technology and Electrical Engineering Journal, October 2019.

3. "Modified Linear Block Code with Code Rate 1/2 and less than 1/2", Journal of Discrete Mathematical Sciences and Cryptography, Taylor & Francis Publication series, March 2019.

4. "Battery Management System – Hardware Design", International Research Journal of Engineering and Technology, Volume: 09 Issue: 04 Apr 2022.

National Journals-

Conferences

1. "A Modified Block Code using Controllability of Linear System", IEEE International Conference on Advances in Computing, Communication and Control (ICAC3), 1-2 Dec. 2017.

2. "Decoding Algorithm for Error Correcting Codes with Syndrome Polynomial", IEEE International Conference on Advances in Computing, Communication and Control (ICAC3), December 2019.

3. "Various Approaches of Decoding Existing Error Correcting Codes: A Survey of Literature" IEEE International Conference on Electrical, Communication, Electronics, Instrumentation and Computing, 30th & 31st January 2019.

4. "Brouwer's fixed point theorem for n-dimension", at the national conference on Emerging Technologies & applicatons in Engineering & Sciences on 3-4   Feb 2011 at Sarswati C.O.E. Kharghar ,Navi Mumbai on 21-22 Dec. 2009.

5. "Topological Theory of fixed and periodic points", in the National conference on Emerging Trends in Computer Technology at Sarswati C.O.E. Kharghar ,Navi Mumbai on 21-22 Dec. 2009.

Books/Book Chapters
1.

Patents/Copy Rights
1. "3 Bit Error Correcting Modified Linear Block Code", application no. 202221010400, patent no. 415002, an Indian patent.

No of Research / consultancy / projects completed: Rs:-	No of Research / consultancy / projects on-going: Rs:-	No of Research / consultancy / projects on applied as on date: Rs:-

Details of Research / consultancy / projects:--

FDPs/Seminars/Workshops/Training Programs Attended/ Organized/ Delivered

Attended
1. Attended ISTE approved one week STTP on "Emerging Trends in Engineering education and teaching methodology and interpersonal skills".
2. Attended I.S.T.E approved S.T.T.P on "Sun rising Technologies in Environmental & Emerging Sciences and soft skill workshop".
3. Attended workshop on "Scilab An Open Source platform for numerical computation" during 13-17 June 2011.
4. Attended one day workshop on SEQUEL on September 03, 2011.
5. Attended I.S.T.E approved S.T.T.P on "Speech, music processing and machine Learning: Fundamentals, Applications and Trends" from 29 DEC-3 JAN 2015.
6.
7. Attended I.S.T.E and AICTE approved S.T.T.P on "Industrial Automation: Hands-on" during 4th - 8th June, 2018.
8.
9. Attended "IUCEE International Engineering Educator workshop", during December 18-20, 2018, Arizona State University.
10.
11. Attended I.S.T.E and AICTE approved S.T.T.P on "Bioinformatics: Emerging Research Trends and Applications" during June 27-July 1, 2017.

Organized
1. Organized one day workshop on "Engineering Electromagnetic Practical Approach" on September 01, 2007.
2. Organized one day workshop on "Power Electronics Syllabus Depth" in association with ISTE. Teacher's Chapter KJSCE. On 4 march 2006.
3. Organized I.S.T.E and AICTE approved S.T.T.P on, "Linear Algebra, Statistics, Mathematical Modelling: Hands On" during 8-12 Jan 2019.
4. Organized guest lecture on "Recent trend in Antenna" for TY students on 12th April 2021 at KJSCE, Mumbai.
5. Organized a 3 days workshop on "Industry 4.0 and Smart Manufacturing", from 2/02/2023 to 4/02/2023.

Delivered:-

Notable Key Scholastic Achievements

1.	Completed one month course on linear block codes with an elite grade from IIT Kanpur.
2.	Completed one month course on Convolutional codes from IIT Kanpur.

Notable Positions and Responsibility

1.	Reviewer for IET Communications
2.	Associate HoD, Department of Electronics Engineering, since 01 July 2021.

Date: 08/02/ 2023   **Signature of Faculty Member**