

## Somaiya Vidyavihar University

<b>Name: Dr. Sandeep Ramesh Sainkar</b>			<b>E-mail:sandeepsainkar@somaiya.edu</b>		
<b>Contact No: 8652457281/ 022-66449308</b>					
Department/Section: Electronics & Telecommunication Engineering					
College: K J Somaiya College of Engineering					
DOJ Somaiya:13/07/2007	Career Experience: 19 Yrs	Industry Experience: 1Yrs	Teaching Experience: 18 Yrs		
Present Academic Designation: Assistant Professor			Present Administrative Designation:		

<b>Area of research/specialization and Courses Delivered</b>
<p>Research domain/interests/areas</p> <ol style="list-style-type: none"> <li>1. Microstrip Antennas</li> <li>2. RF/Microwave Electronics</li> <li>3. Analog Electronics</li> <li>4. Electromagnetics</li> </ol> <p>Courses Delivered</p> <ol style="list-style-type: none"> <li>1. Basic Electronics Circuits</li> <li>2. Analog Electronics</li> <li>3. Electromagnetic field theory</li> <li>4. Communication Systems</li> <li>5. Microprocessor and Microcontroller</li> <li>6. RF Modeling and Antennas</li> <li>7. Television Engineering</li> <li>8. Microwave Engineering</li> <li>9. Broadband Communication</li> </ol>

Recognition as a teacher by any University	UG: Yes	PG: No	Ph.D : No
<p>Details of Recognitions</p> <ol style="list-style-type: none"> <li>1. Mumbai university Concol letter no. CONCOL/SA/1000/2009 dated March 12,2009</li> </ol>			

<b>Education</b>					
Examination	Name of the Degree	University/Board	Institute/College	Year	CPI/SPI/ %Marks
Ph.D	Ph.D. (Electrical Engg.)	Mumbai	Veermata Jijabai Technological Institute (VJTI), Mumbai	2022	
PG	M.Tech. (Electronics Engg.)	Mumbai	Veermata Jijabai Technological Institute (VJTI), Mumbai	2011	8.7 CPI
UG	B. E. (Electronics Engg.)	Mumbai	Vivekanand Education Societys' Institute of Technology (VESIT), Mumbai	2002	60.80 %
Diploma	-	-	-	-	
NET/SET/Other	-	-	-	-	

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<b>Notable Experience Details</b>					
Sr. No	Name of the organization	Designation	Date of Joining	Date of Leaving	Experience (Years)
1.	Fr. C. R.I.T., Vashi	Lecturer	20/01/2004	12/07/2007	3.5
2.	K J Somaiya College of Engineering				

<b>Research Accomplishments and Projects</b>		
No of students pursuing Ph.D as on date: NIL		No of students completed Ph.D as on date:
No of students completed PG thesis / Project work as on date: NIL		No of students / groups completed UG projects as on date: 15
Publications Total: 10	Number of Peer review Journal papers: 2	Number of Conference papers: 8

### Details of Publications:

#### International Journals

1. Sandeep R. Sainkar, Aviraj Jadhav, Alice N. Cheeran, J. John, Promod K. Sharma, Harish V. Dixit, "Design of a 3.7 GHz, 1 kW CW, Hybrid Radial Power Divider for LHCD System of SST-1 Tokamak Fusion Engineering and Design, Elsevier, <https://doi:10.1016/j.fusengdes.2021.112864>. vol. 173 ,112864, Sep. 2021, **(SCI and Scopus Indexed, IF: 1.692)**
2. Sandeep R. Sainkar, Alice N. Cheeran, Promod K. Sharma, Harish V. Dixit "Design of the 3.7 GHz, 1 kW CW solid state driver for LHCD system of the SST-1 tokamak", Fusion Engineering and Design, Elsevier, <https://doi.org/10.1016/j.fusengdes.2020.111692>, ISSN 0920-3796, vol. 158, April 2020 **(SCI and Scopus Indexed, IF: 1.692)**
3. Sandeep Sainkar, Amutha Jeyakumar, "Design analysis Of Broadband Circularly Polarized Compact Microstrip Antenna For Wireless Applications", International Journal of Electronics & Communication Technology (IJECT), Vol. 2, Issue 2, June 2011.

#### National Journals

NIL

#### Conferences

1. Sandeep R. Sainkar, Alice N. Cheeran, Promod K. Sharma, Harish V. Dixit, "Performance Evaluation of a 3.7 GHz, 1 kW CW solid state source for LHCD system of the SST-1 tokamak", in 47<sup>th</sup> IEEE International conference on Plasma Sciences(ICOPS 2020), 6- 10 December 2020.
2. Sandeep R. Sainkar, Alice N. Cheeran, Gajendrakumar Shinde, Promod K. Sharma, Harish V. Dixit, "Practical Considerations for the design of 3.7 GHz CW Power Amplifier", 2020 IEEE International RF and Microwave Conference (RFM-2020), 14-16 December 2020. **(SCI and Scopus Indexed)**
3. Design Analysis and Testing of Broadband Circularly Polarized Compact Microstrip Antenna for Wireless Applications, 2011 International Conference on Signal Processing, Communication, and Networking technologies (ICSSN 2011), Nooral Islam centre for Higher Education, Kumaracoil, Tamilnadu ,20-21 July, 2011.

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4. Sandeep R. Sainkar, Snehal D., Harish V. Dixit , Alice N. Cheeran, Promod K. Sharma , “Efforts In Indigenous Design And Development Of High Power Microwave Devices” , jointly organised by Institute for Plasma Research Gandhinagar and Nirma University Ahmedabad, NFP-PFRC (NPVM-2018} , 24- 26 April 2018.
5. Sandeep R. Sainkar, Alice N. Cheeran, Promod K. Sharma, Harish V. Dixit , “Design Of A 3.7 GHz Oscillator For The Solid State Drive Of The LHCD System”, 32<sup>nd</sup> National Symposium on Plasma Science & Technology (PLASMA 2017), 7-10 November 2017.
6. Paper Presented and Published on “Analysis of Circularly Polarized Compact Microstrip Antenna For Broadband Applications”, at National Conference ETCC- 2010, organized by Medicaps Institute Of Technology and Management, Indore from 19-20<sup>th</sup> November 2010.
7. Paper Presented and Published on “Analysis of single fed gap coupled CP Microstrip antenna for broadband applications”, at National Conference NCICT-2006, organized by D.J.Sanghvi College of Engineering, Vileparle from 1-3<sup>rd</sup> March 2006.

### Books/Book Chapters

1.

### Patents/Copy Rights

1.

No of Research / consultancy / projects completed: 3  
Rs: 35000/-, Rs. 24.2 lacs

No of Research / consultancy / projects on-going:  
Rs: \_\_\_\_\_

No of Research / consultancy / projects on applied as on date:  
Rs: \_\_\_\_\_

### Details of Research / consultancy / projects:

#### Completed

1. **“Design and development of active & passive devices for enhancement of Antenna & Microwave laboratory facility”**, Principal Investigator, Minor research grant sanctioned by University of Mumbai for the financial year 2016-17.
2. **“Design and development of 3.7 GHz 1 kW CW solid state driver for the LHCD system”**, Co-principal Investigator, Major research grant sanctioned by BRNS, DAE for the financial year 2018-19.
3. **“Design and development of Radial power combiner and divider for high power microwave applications”**, Principal Investigator, Minor research grant sanctioned by University of Mumbai for the financial year 2019-20.

#### On-going

#### Applied

### IPR/ Copyrights

1.

### FDPs/Seminars/Workshops/Training Programs Attended/ Organized/ Delivered

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### Attended

1. Attended a workshop on “ELECTROMAGNETICS AND MICROWAVE ENGINEERING: PEDAGOGY, RESEARCH TRENDS AND APPLICATIONS (EMPRA 2023)”, organized by BITS-Pilani , Hyderabad, 01-03 November, 2023
2. Delivered lecture on “Active RF Devices for IOT applications”, at STTP organized by K. J. Somaiya College of Engineering, Vidyavihar, 04th January, 2023
3. Attended a workshop on “ELECTROMAGNETICS AND MICROWAVE ENGINEERING: PEDAGOGY, RESEARCH TRENDS AND APPLICATIONS”, organized by BITS-Pilani , Hyderabad, 02-04 January, 2020
4. Attended one week workshop on “Practical RF / Microwave Active Circuit Design” BITS-Pilani, Hyderabad, January 02 – January 06 , 2019
5. Attended one week Short Term Training Program on “Active RF and Microwave Circuit Design” , R.V. College of Engineering, Bangalore, February 26 – March 03, 2018
6. Attended one week AICTE sponsored QIP Short Term Training Program on “Antenna, RF and Microwave System Design” ,VJTI Mumbai, March 08 – 13 , 2016
7. Attended two day Workshop on ADS Software at KJSCE, Vidyavihar, EXTC Dept., 18<sup>th</sup> – 19<sup>th</sup> April 2014
8. Attended two week Short Term Training Program on “Materials Engineering And Industrial Application: Hybrid Nano Composites For Photonics, Energy And Electronic Devices” organized by Material Science Centre, IIT Kharagpur, November 11 – 22 , 2013
9. Attended a two week ISTE workshop on “Introduction to Research Methodologies” conducted by IIT-Bombay, MHRD at K.J.Somaiya College of Engineering during 25 June- 04 July 2012
10. Attended a two day workshop on “Writing Effective Conference Paper” conducted by IIT Bombay, at K.J.Somaiya College of Engineering during 18- 19 Feb 2012.
11. Attended one week Short Term Training Program on “Upcoming trends in Microwave Engineering”, organized by KJSCE, Vidyavihar, Mumbai, January 2009.

### Organized

1. Organised one week Short Term Training Program on “**Internet of Things: A Multidisciplinary Approach for Future Innovations**”, at KJSCE, Vidyavihar, Mumbai, 02- 06 January, 2023.
2. Organised one week Short Term Training Program on “Advances in Antenna & MMIC/RFIC Design”, at KJSCE, Vidyavihar, Mumbai, 24<sup>th</sup> - 28<sup>th</sup> Jun, 2013.
3. Organized workshop on NEC open source antenna software for KJSCE EXTC students, 2<sup>nd</sup> Feb, 2013

### Delivered

1. Delivered lecture on “ Microwave Amplifier Design Considerations-a Practical approach”, at STTP organized by K.. J. Somaiya Institute of Engineering and Technology, Sion, 04th July, 2019

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2. Delivered lecture on “Design Considerations of RF/Microwave active devices”, at STTP organized by BITS-Pilani , Hyderabad, 03<sup>rd</sup> January, 2019
3. Delivered lecture on “Design and Simulation of RF active devices using AWR Microwave office” at BITS-Pilani , Hyderabad, 17<sup>th</sup> Nov, 2018

### Notable Key Scholastic Achievements

1.	
2.	

### Notable Positions and Responsibility

1.	Worked as Department Placement and Internship Coordinator
2.	Working as OE coordinator at institute level
3.	Working as task force member for NEP at SVU campus level

**Date:** 05 /01 / 2024

**Signature of Faculty Member**

**Dr. Sandeep Sainkar**

**Assistant Professor,**

**Department of Electronics and Telecommunication Engineering, KJSCE**

## **Details of Publications:**

### **Journals**

1. Sandeep R. Sainkar, Aviraj Jadhav, Alice N. Cheeran, J. John, Promod K. Sharma, Harish V. Dixit, "Design of a 3.7 GHz, 1 kW CW, Hybrid Radial Power Divider for LHCD System of SST-1 Tokamak Fusion Engineering and Design, Elsevier, <https://doi.org/10.1016/j.fusengdes.2021.112864>. vol. 173 ,112864, Sep. 2021, **(SCI and Scopus Indexed, IF: 1.692)**
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7. Sandeep R. Sainkar, K T V Reddy, “Analysis of single fed gap coupled CP Microstrip antenna for broadband applications”, at National Conference NCICT-2006, organized by D.J.Sanghvi College of Engineering, Vileparle from 1-3<sup>rd</sup> March 2006.