

Somaiya Vidyavihar University

Name: Mrs.Shilpa N Vatkhar		E-mail: shilpavatkar@somaiya.edu	
Contact No: 022-27572924			
Department/Section: Electronics &Telecommunication)			
College: K J Somaiya College of Engineering			
DOJSomaiya: 6/2/2012	Career Experience:15 Yrs	Industry Experience: NIL	Teaching Experience:_15 Yrs
Present Academic Designation: Assistant Professor)		Present Administrative Designation: (Principal/Vice-Principal/ Associate Dean/ HOD etc)	

Area of research/specialization and Courses Delivered
Research domain/interests/areas 1. VLSI 2. Image processing Courses Delivered 1. VLSI design 2. Operating system, 3. Data Structure analysis and algorithm 4. Integrated circuit, 5. Microprocessor and Microcontroller, 6. Microelectronics, 7. Analog CMOS VLSI

Recognition as a teacher by any University	UG: Yes	PG: No	Ph.D : /No
Details of Recognitions 1. UGC faculty of SUV University 2.			

Education					
Examination	Name of the Degree	University/Board	Institute/College	Year	CPI/SPI/ %Marks
Ph.D pursuing	PH.D electronics and telecommunication	Mumbai University	Sardar Patel college of Engg, Andheri		
PG	MTECH in VLSI	RTM Nagpur	Priyadarshini college of engg, Nagpur	2009	68%
UG	B.E in Electronics &Telecommunication	Amravati University	Sant Gajanan Maharaj college of Engg, Shegaon	1999	62%
Diploma					
NET/SET/Other					

Notable Experience Details					
Sr. No	Name of the organization	Designation	Date of Joining	Date of Leaving	Experience (Years)
1.	K.J.Somaiya college of engg, Mumbai	Assistant professor	6/02/2012	Till date..	11 yrs
2.	Vincent college of Engg, Nagpur	Assistant professor	25/01/2001	18/12/2010	4 yrs

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Research Accomplishments and Projects		
No of students pursuing Ph.D as on date:		No of students completed Ph.D as on date:
No of students completed PG thesis / Project work as on date:		No of students / groups completed UG projects as on date:20
Publications Total:	Number of Peer review Journal papers:	Number of Conference papers:
Details of Publications: International Journals: FPGA-based Controller for a Mobile Robot, (IJCSIS) International Journal of Computer Science and Information Security, Vol. 3, No. 1, 2009 National Journals Conferences Books/Book Chapters		
Patents/Copy Rights		
No of Research / consultancy / projects completed: Rs:	No of Research / consultancy / projects on-going: Rs:	No of Research / consultancy / projects on applied as on date: Rs:
Details of Research / consultancy / projects: Completed On-going Applied		
IPR/ Copyrights		

FDPs/Seminars/Workshops/Training Programs Attended/ Organized/ Delivered
Attended <ol style="list-style-type: none"> 1. FDP on “VLSI chip Design Hands on using open source EDA organized by electronics and ICT.Dec 2019, erna Engg college, Navi Mumbai 2. IETE Approved One week STTP On Advanced VLSI Design and Applications with hands-on in CADENCE toolset,2018, Shah & Anchor Kutchhi Engineering College, Chembur. 3. Workshop on CMOS Mixed signal and Radio Frequency VLSI design 2016 ,IIT kharagpur 4. Short term training program Micro Electro-Mechanical- Systems, Shri Ramdeobaba College of Engineering and Management., 2015 5. Workshop on Labview,KJSCE Mumbai, 2014. 6. Workshop on “emerging Trends in Embedded Systems”, PCE Nagpur 7. Online FDP on Advanced Technologies in IT, 2020. 8. Introduction to the Certified Peer Reviewer Course, Researcher Acedemy, 2020
Organized <ol style="list-style-type: none"> 1. Workshop on CMOS Mixed signal and Radio Frequency VLSI design, one week ,KJSCE<EXTC. 2.
Delivered <ol style="list-style-type: none"> 1. Two days training on “Refresher course on Data structures and object oriented programming Language“, EXTC,KJSCE. 2.

Notable Positions and Responsibility	
1.	
2.	

Date: 07/ 02/ 2023

Signature of Faculty Member:Shilpa Vatkar