

Somaiya Vidyavihar University

Name: Inderjit Singh Dhanjal			E-mail: inderjitsingh@somaiya.edu		
Contact No: +91-9769390107					
Department/Section: Electronics Engineering					
College: K J Somaiya College of Engineering					
DOJ	Somaiya:	Career Experience:	Industry Experience: Nil	Teaching Experience:	
3.1.2018		5 + Yrs		11.5 Yrs	
Present Academic Designation: Assistant Professor			Present Administrative Designation: Nil		

Area of research/specialization and Courses Delivered
<p>Research domain/interests/areas</p> <ol style="list-style-type: none"> 1. Analog Electronics 2. Analog and Digital VLSI 3. Simulation of circuits using open source software <p>Courses Delivered</p> <ol style="list-style-type: none"> 1. Electronics Circuit analysis and Design 2. Basics of VLSI Design 3. Power Electronics 4. Linear Integrated circuit design 5. Analog VLSI 6. Basics of Electronics Circuit 7. Analog Electronic Circuit 8. Industrial Electronics

Recognition as a teacher by any University	UG: Yes	PG: Yes/No	Ph.D : No
Details of Recognitions <ol style="list-style-type: none"> 1. Concol Letter of recognition as teacher by Mumbai University. 			

Education					
Examination	Name of the Degree	University/Board	Institute/College	Year	CPI/SPI/ %Marks
Ph.D	NA	NA	NA	NA	NA
PG	Master of Engineering (Electronics Engineering Branch)	University of Mumbai	Fr. Conceicao Rodrigues College of Engineering	2016	CGPI-9.35
UG	Bachelor of Engineering (Electronics Engineering Branch)	University of Mumbai	Vidyalankar Institute of Technology	2009	75.83 % First Class with Distinction
Diploma	NA	NA	NA	NA	NA
NET/SET/Other	NA	NA	NA	NA	NA

Notable Experience Details					
Sr. No	Name of the organization	Designation	Date of Joining	Date of Leaving	Experience (Years)
1.	K.J Somaiya College of Engineering, Electronics Engineering Department	Assistant Professor (Regular)	3.1.2018	Ongoing	5
2.	Fr. Conceicao Rodrigues College of Engineering	Teaching assistant	July 2012	April 2013	1
3.	D.J Sanghvi College of Engineering, Electronics	Lecturer (Adhoc) Assistant Professor	July 2010 July 2011	June 2011 June 2012	5.5

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Engineering Department	(Adhoc)	July 2013	December 2017
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Research Accomplishments and Projects		
No of students pursuing Ph.D as on date: Nil		No of students completed Ph.D as on date: Nil
No of students completed PG thesis / Project work as on date: Nil		No of students / groups completed UG projects as on date: 8 groups
Publications Total: 4	Number of Peer review Journal papers: 3	Number of Conference papers: 1
Details of Publications: International Journals <ol style="list-style-type: none"> 1. Sagar Korde, Inderjit Singh Dhanjal, Parth Chaudhary, Dishika Mehta & Binita Soni. (2023). Impact of Entrepreneurship Ecosystem in an Engineering College: A Case Study. <i>Journal of Engineering Education Transformations (JEET)</i>, 36(Special Issue 2):292-296. 10.16920/jeet/2023/v36is2/23042 2. Sonia Joshi, Deepa Jain & Inderjit Singh Dhanjal. (2022). Case Study: Integrated In-house Textbook Companion Internship. <i>Journal of Engineering Education Transformations (JEET)</i>, 35(Special Issue 1):47-52. 10.16920/jeet/2022/v35is1/22007 3. Amit Dhiman, Neel Shah, Sayali Kumbhar, Pranali Adhikari, Ninad Mehendale & Inderjit Singh Dhanjal. (2021). Firefighting robot with deep learning and machine vision. <i>Neural Computing and Applications</i>, 34(4):2831–2839. https://doi.org/10.1007/s00521-021-06537-y National Journals <ol style="list-style-type: none"> 1. Nil Conferences <ol style="list-style-type: none"> 1. Inderjit Singh Dhanjal, Sangeeta Kulkarni, Ajay Gangrade, Arati Phadke & Manish Potey. Development of Entrepreneurship Mindset of Students through Curriculum: A Case Study. <i>Ninth International Conference on Transformations in Engineering Education</i>, Online, January 08-09, 2022. Books/Book Chapters <ol style="list-style-type: none"> 1. Nil Patents/Copy Rights <ol style="list-style-type: none"> 1. Nil 		
No of Research / consultancy / projects completed: Rs: 0	No of Research / consultancy / projects on-going: Rs: 0	No of Research / consultancy / projects on applied as on date: Rs: 0
Details of Research / consultancy / projects: Completed <ol style="list-style-type: none"> 1. Fever Kiosk On-going <ol style="list-style-type: none"> 1. Nil Applied <ol style="list-style-type: none"> 1. Nil 		
IPR/ Copyrights <ol style="list-style-type: none"> 1. Nil 		

FDPs/Seminars/Workshops/Training Programs Attended/ Organized/ Delivered
Attended <ol style="list-style-type: none"> 1. Click on link to view
Organized <ol style="list-style-type: none"> 1. Nil
Delivered <ol style="list-style-type: none"> 1. Nil

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Notable Key Scholastic Achievements	
1.	Adjudged in Top 4 Outstanding category circuits in three weeks Mixed Signal SoC design Marathon using eSim and SKY130, an initiative of the FOSSEE (Free/Libre and Open Source Software for Education) Project, Indian Institute of Technology Bombay in association with VLSI System Design Corp.Pvt.Ltd and Google Open Source. The marathon was conducted in the month of September to October 2022 for duration of 3 weeks. https://hackathon.fossee.in/esim/results
2.	Adjudged in very Good category circuits in two weeks Capture the Bug, A Design Verification Hackathon, organized by NIELIT Caicut and technically facilitated by Vyoma systems, VLSI system Design and mentored by IIT Madras & IEEE Robotics and Automation society, Kerala Chapter. The hackathon was conducted from 15th July to 1st August 2022 Certificate
3.	Adjudged in excellent category circuits in three weeks Mixed Signal Circuit Design and Simulation Marathon using eSim, organized by FOSSEE (Free/Libre and Open Source Software for Education) Project, IIT Bombay in association with VLSI System Design Corp.Pvt.Ltd and Redwood EDA. The marathon was conducted in the month of Feb-March 2022 for a duration of 3 weeks. https://hackathon.fossee.in/esim/feb22/results
4.	Adjudged in very Good category circuits in two weeks Cloud Based Analog IC Design Hackathon, an initiative of IIT Hyderabad, which has been sponsored by Synopsys in association with VLSI System Design (VSD) Pvt. The hackathon was conducted from 15 th Feb to 1 st March 2022 https://hackathoniith.in/result-verygood
5.	Adjudged in Top 10 Outstanding category circuits in two weeks Marathon on Circuit Design and Simulation using eSim, an initiative of the FOSSEE (Free/Libre and Open Source Software for Education) Project, Indian Institute of Technology Bombay in association with VLSI System Design Corp.Pvt.Ltd. The marathon was conducted from 17th June 2021 to 30th June 2021 https://hackathon.fossee.in/esim/2021/results
6.	Secured AB grade in “Solid State Devices”, an Post graduate level course conducted by EE department, IIT Bombay under CEP scheme from July to November 2014 (Certificate Link)
7.	Secured 10th position in the ORDER of MERIT in Mumbai University for B.E Electronics SEM VIII May 2009 Electronics Engineering Branch Examination (Link)
8.	Recipient of Sir JRD Tata Scholarship Grant for Academic excellence for the year 2008-2009 (Link)

Notable Positions and Responsibility	
1.	Faculty In charge: Bloombox-Entrepreneurship Cell of K.J. Somaiya College of Engineering
2.	Faculty In charge: Electronics Engineering Students Association (EESA)
3.	Faculty Co-ordinator: Laboratory planning, Development, Budget, and Purchase
4.	Departmental Honour’s program coordinator

Date: 08/02/2023

Inderjit Singh Dhanjal
Signature of Faculty Member